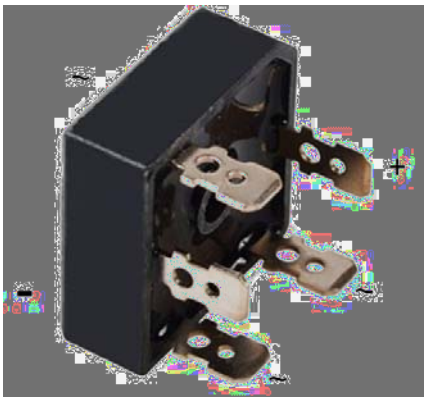
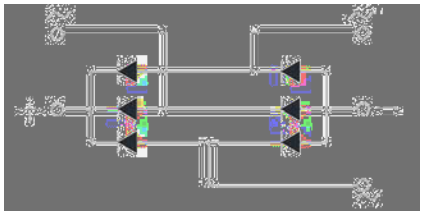




- UL recognition, file #E230084
- Low thermal resistance
- Glass passivated chip junction
- High surge current capability
- Solder dip 275 °C max. 7 s, per JESD 22-B106

General purpose use in AC/DC bridge full wave rectification for

- : SKBPC
  Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant
- : Tin plated leads, solderable per J-STD-002 and JESD22-B102
- As marked on body

■ (T_a=25 Unless otherwise specified

Device marking code			SKBPC5018
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	V	1800
Maximum RMS Voltage	V_{RMS}	V	1260
Maximum DC blocking Voltage	V_{DC}	V	1800
Average rectified output current @60Hz sine wave, R-load (With heatsink Tc =55)	I_O	A	50
Forward Surge Current (Non-repetitive) @8.3ms Half-sine wave,1 cycle, Tj=25	I_{FSM}	A	500
Current squared time @1ms≤t≤8.3ms Tj=25 , Rating of per diode	I^2t	A^2s	1038
Storage temperature	T_{stg}		-55 ~ +150
Junction temperature	T_j		-55 ~ +150
Dielectric strength @ Terminals to case, AC 1 minute	V_{dis}	KV	2.5
Mounting torque @Recommend torque 5kg cm	Tor	kg cm	8

■ T_a=25 Unless otherwise specified

Maximum instantaneous forward voltage drop per diode	V_F	V	I_{FM}=25A	1.15
Maximum DC reverse current at rated DC blocking voltage per diode	I_R	μA	T_j=25	5
			T_j=125	500
Typical junction capacitance	Cj	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	175

■ T_a=25 Unless otherwise specified

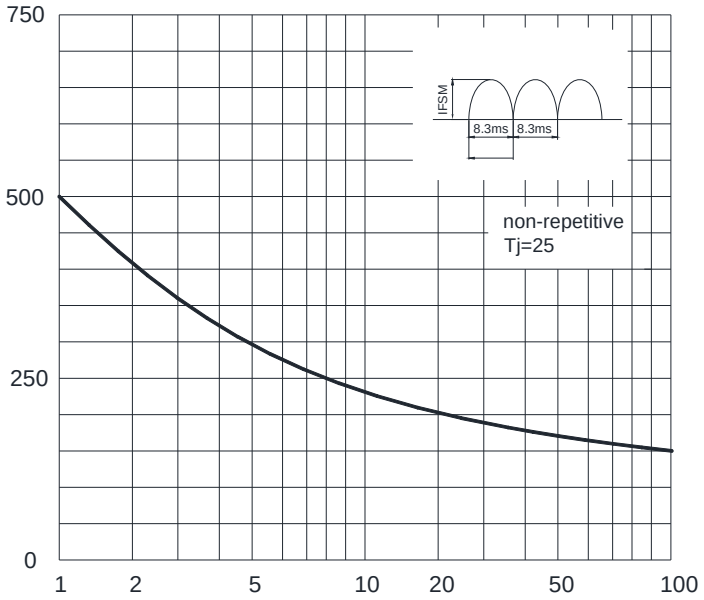
Typical Thermal Resistance	Between junction and case, With heatsink	$R_{\theta J-C}$	/W	0.9

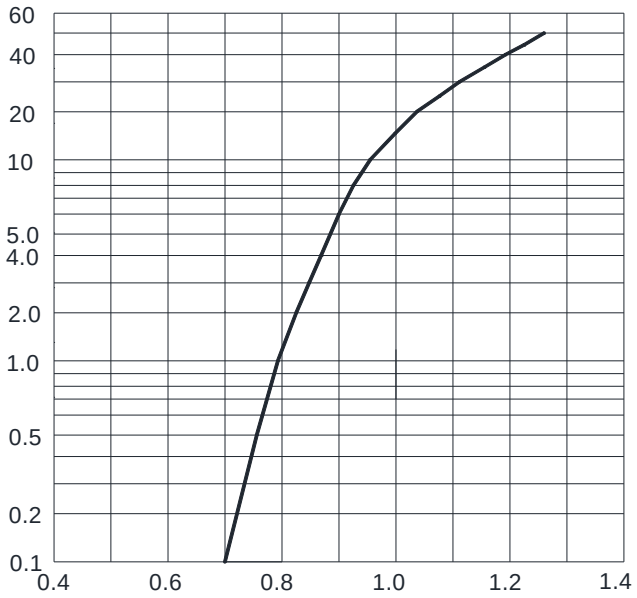
Note: Device mounted on 75mm x 45mm x 5.5mm Aluminum Plate Heatsink.

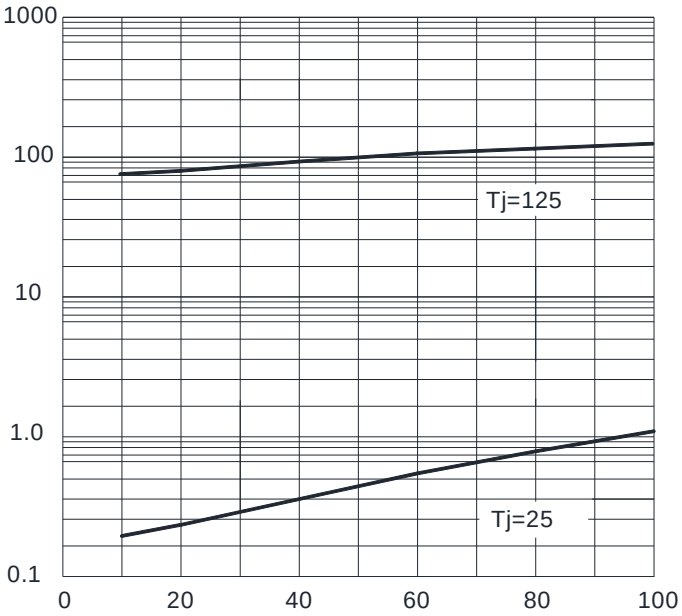
■ (Example)

	A1	Approximate 19	50	50	500	Paper Box

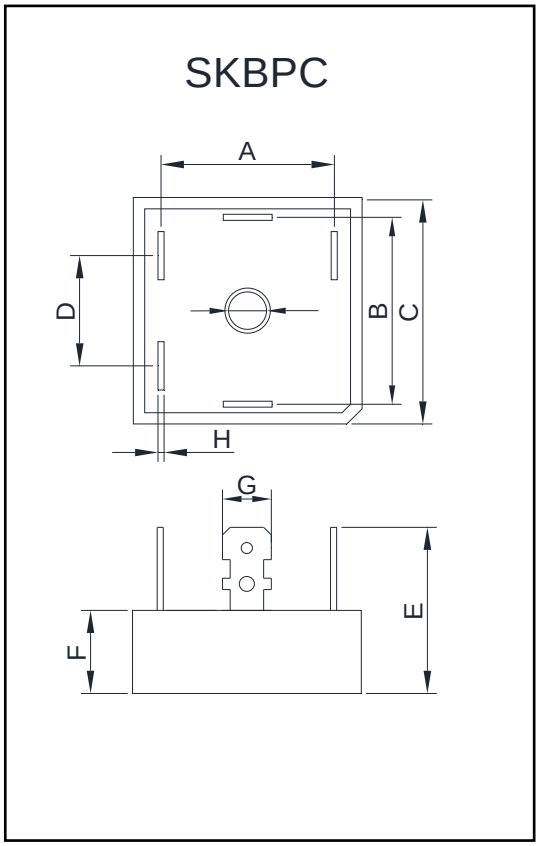
■ (Typical)







■



A	23.1	24.1
B	23.1	24.1
C	28.2	28.8
D	16.0	17.0
E	/	25
F	10.8	11.2
G	6.2	6.4
H	0.75	0.85
I	4.5	5.5

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