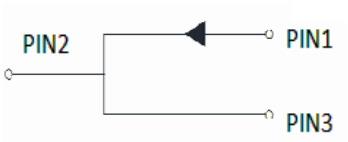
RoHS
COMPLIANT



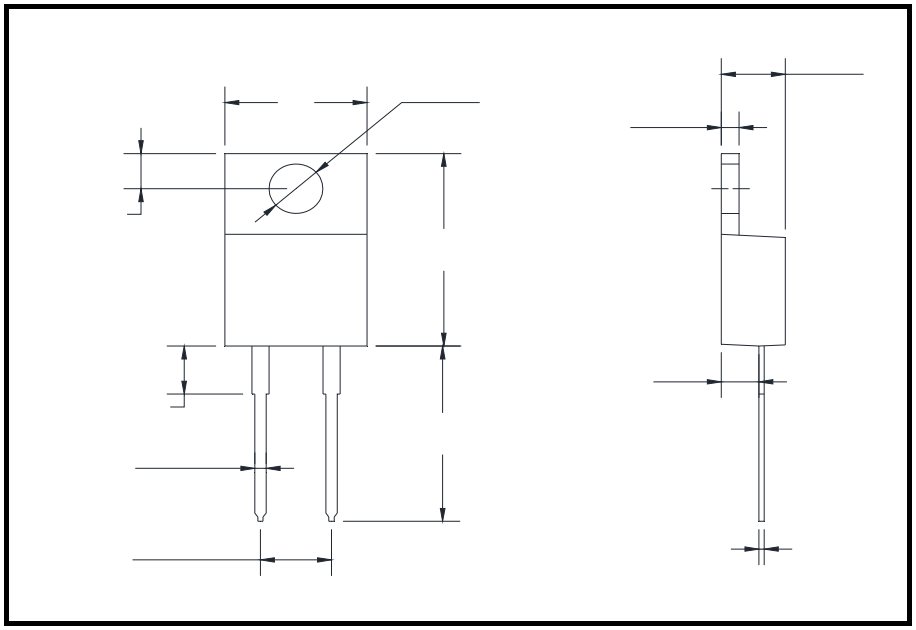


- Adopt FRED chip
- Low forward Voltage drop
- Fast reverse recovery time
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Terminal leads: Tin plated leads solderable per J-STD-002 and JESD22-B102
- : As marked

■ (Tj=25 Unless otherwise specified

Device marking code			MUR860U
Repetitive Peak Reverse Voltage	V_{RRM}	V	600
Average Rectified Output Current @60Hz sine wave, R-load, T_C(FIG.1)	I_O	A	8
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, T_j=25	I_{FSM}	A	80
Current Squared Time @1ms≤t≤8.3ms Tj=25	I^2t	A^2s	26.56
Storage Temperature	T_{stg}		-55 ~ +175
Junction Temperature	T_j		-55 ~ +175
Typical Junction capacitance @4V,1MHz	Cj	pF	30

■



Dim	Min	Max
A	9.95	10.35
B	2.55	2.95
C	3.75	4.05
D	14.95	15.25
E	3.75	4.25
F	0.26	0.5
G	0.68	0.94
H	13.3	13.9
I	4.86	5.26
J	4.38	4.78
K	1.14	1.4
L	2.37	2.79

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