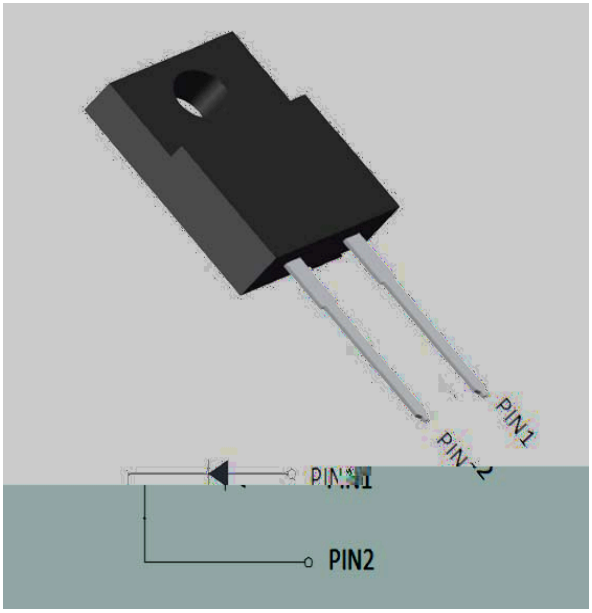


- Adopt FRED chip
- Low forward Voltage drop
- Fast reverse recovery time
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability

Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

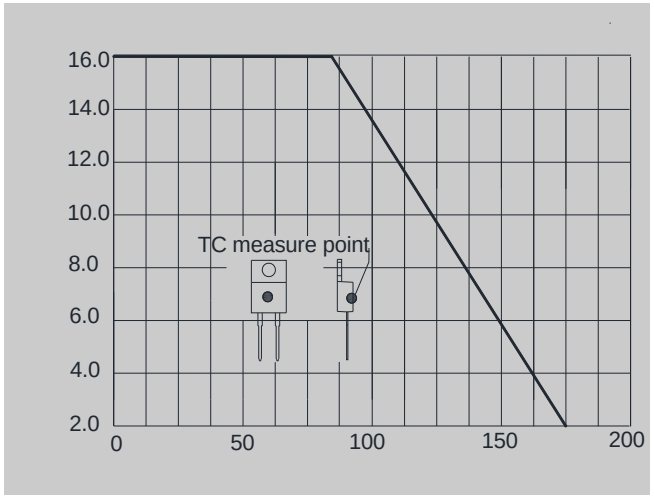
- : ITO-220AC
  Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant
- : Tin plated leads, solderable per J-STD-002 and JESD22-B102
- : As marked

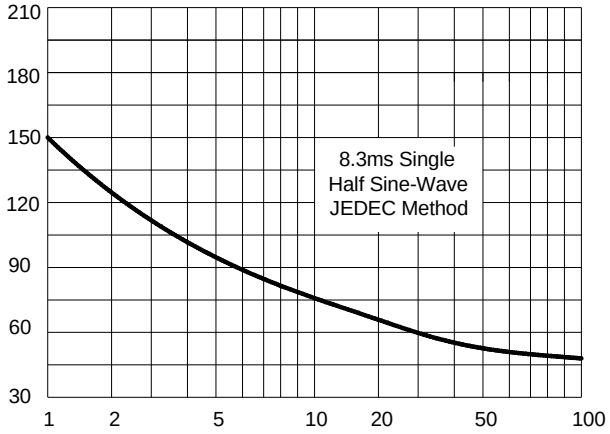
■ (Tj=25 Unless otherwise specified

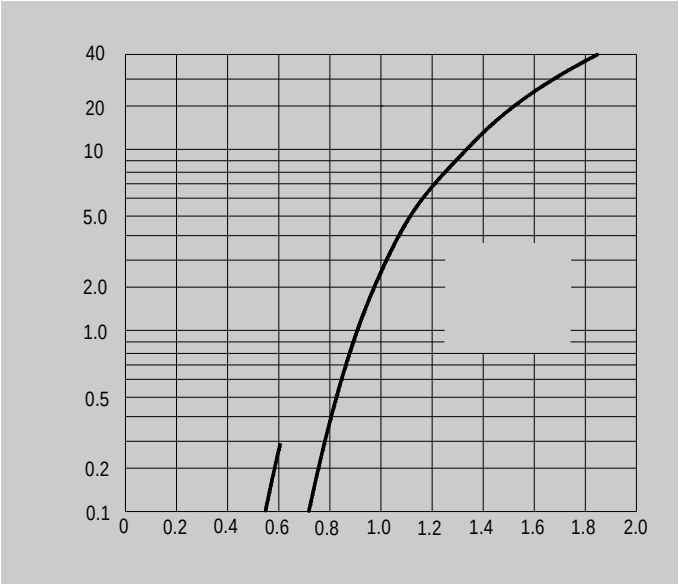
Device marking code			MUR1660F
Repetitive Peak Reverse Voltage	V_{RRM}	V	600
Average Rectified Output Current @60Hz sine wave, R-load, Tc(FIG.1)	I_O	A	16
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, Tj=25	I_{FSM}	A	150
Current Squared Time @1ms≤t≤8.3ms Tj=25	I^2t	A^2s	93
Storage Temperature	T_{stg}		-55 ~ +175
Junction Temperature	Tj		-55 ~ +175
Typical Junction capacitance @4V,1MHz	Cj	pF	98
Mounting torque @recommend torque 5kg cm	Tor	kg cm	8

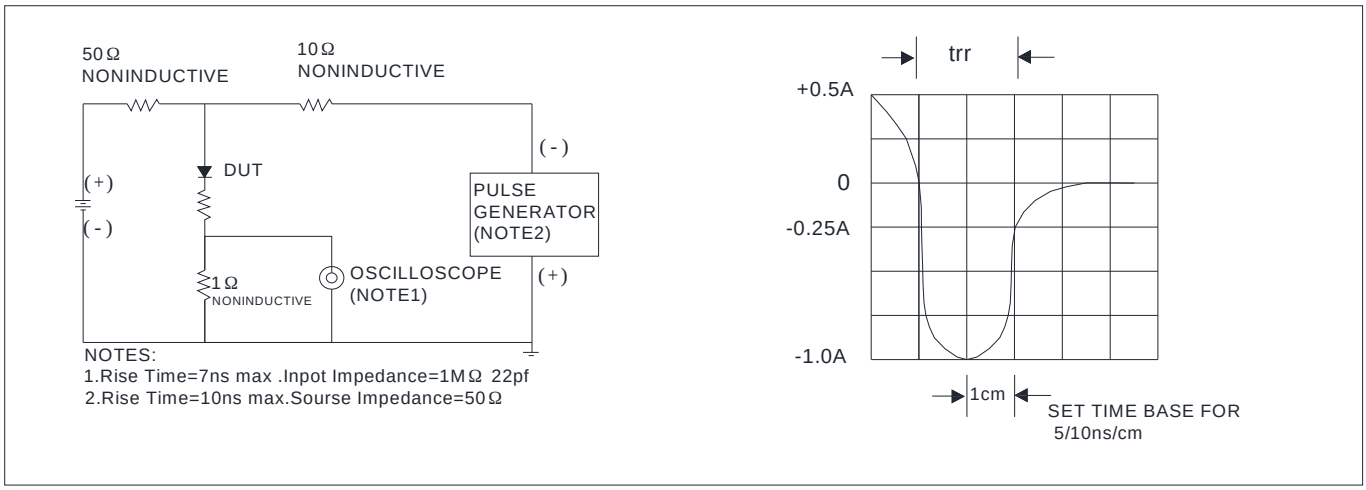
■

■ (Typical)

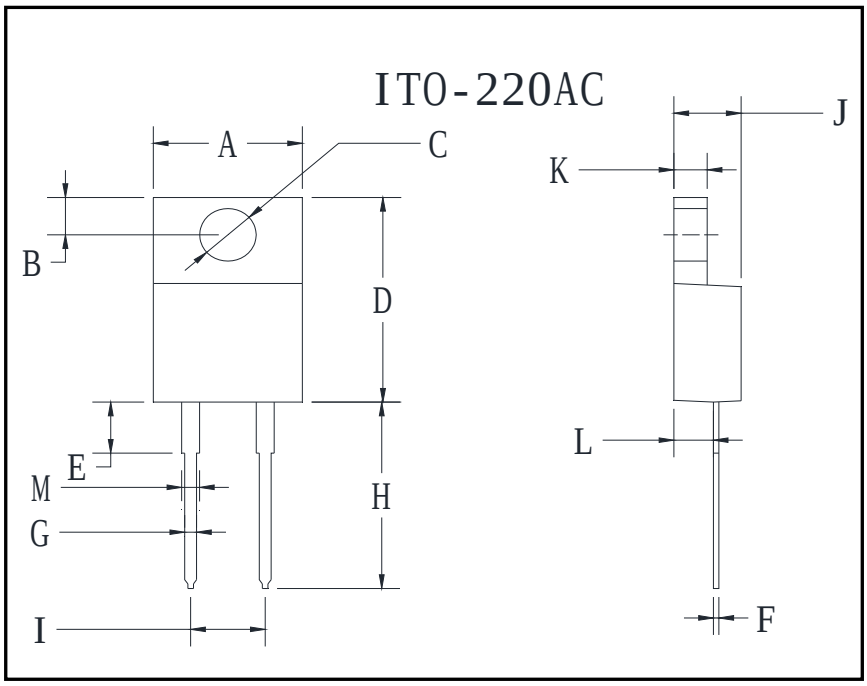

16.0
14.0
12.0
10.0
8.0
6.0
4.0
2.0
0
50
100
150
200
TC measure point


210
180
150
120
90
60
30
1
2
5
10
20
50
100
8.3ms Single
Half Sine-Wave
JEDEC Method


40
20
10
5.0
2.0
1.0
0.5
0.2
0.1
0
0.2
0.4
0.6
0.8
1.0
1.2
1.4
1.6
1.8
2.0


50Ω
NONINDUCTIVE
10Ω
NONINDUCTIVE
DUT
(+)
(-)
(-)
PULSE
GENERATOR
(NOTE2)
1Ω
NONINDUCTIVE
OSCILLOSCOPE
(NOTE1)
(+)
NOTES:
1.Rise Time=7ns max .Inpot Impedance=1MΩ 22pf
2.Rise Time=10ns max.Sourse Impedance=50Ω
trr
+0.5A
0
-0.25A
-1.0A
1cm
SET TIME BASE FOR
5/10ns/cm

■



Dim	Min	Max
A	9.8	10.2
B	2.25	2.75
C	2.95	3.45
D	14.75	15.25
E	3.5	4.1
F	0.45	0.75
G	0.45	0.75
H	13.35	14.15
I	4.97	5.23
J	4.3	4.8
K	2.5	2.74
L	2.58	2.82
M	1.03	1.43

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