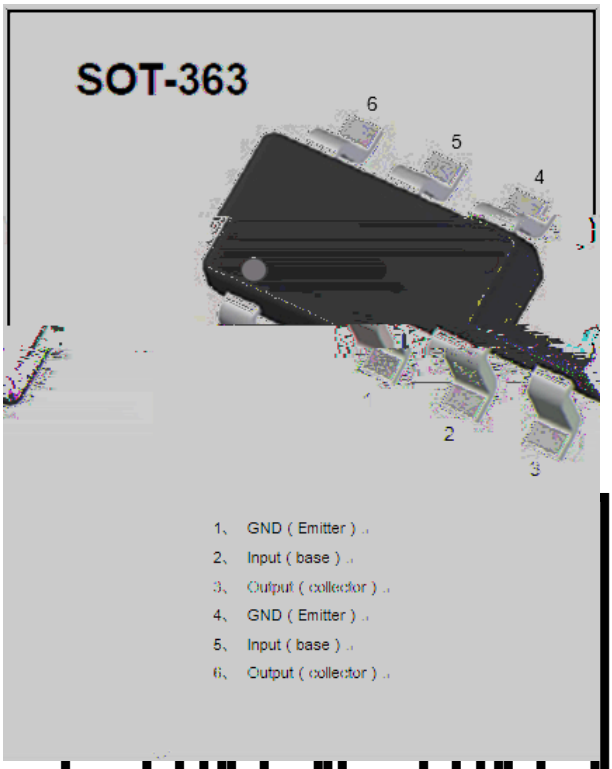
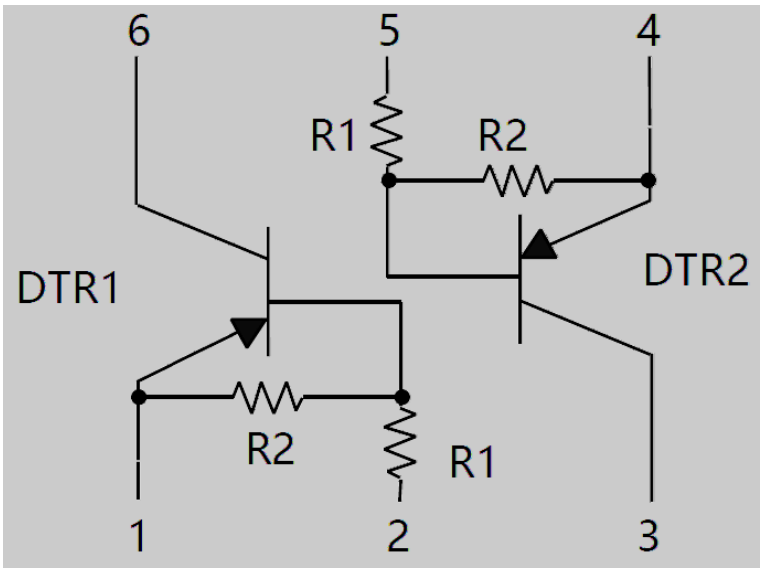
RoHS
COMPLIANT



- Epoxy [illegible] 0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without [illegible] input resistors
- Surface mount package ideally Suited for Automatic Insertion

- SOT-363
- Tin plated leads, solderable per J-STD-002 and JESD22-B102
- [illegible]



■ (Ta=25 Unless otherwise specified

Supply Voltage	V_{CC}	V		-50
Input Voltage	V_{IN}	V		-40 to +6
Output Current	I_O	mA		-100
Power Dissipation	P_D	mW		150
Junction Temperature	Tj			150
Storage Temperature	T_{STG}			-55 to +150

■ (Ta=25 unless otherwise specified)

Input voltage	$VI_{(off)}$	V	V_{CC}=-5V,Ic=-100uA	-0.3	-	-
	$VI_{(on)}$	V	V_O=-0.3V,Ic=-1mA	-	-	-1.4
Output voltage	$Vo_{(on)}$	V	Io /Ii =-5mA/-0.25 mA	-	-	-0.3
Input current	I_I	mA	V_I=-5V	-	-	-0.88
Output curre	$I_{O(o)}$	uA	V_{CC}=-50V, Vi=0	-	-	-0.5
DC current gain	G_I		Vo=-5V, Io =-5mA	68	-	-
Input resistance	R	kΩ		7	10	13
Resistance ratio	R_{2/R_1}			3.7	4.7	5.7
Transition frequency	f	MHz	V_{CE}=-5V,I_E=-5mA,		250	-

■ (

UMB9N	E2	Approximate 0.009g	3000	30000	120000	7” reel

■ (Typical)

Fig. 1 - DC Current Gain Characteristics

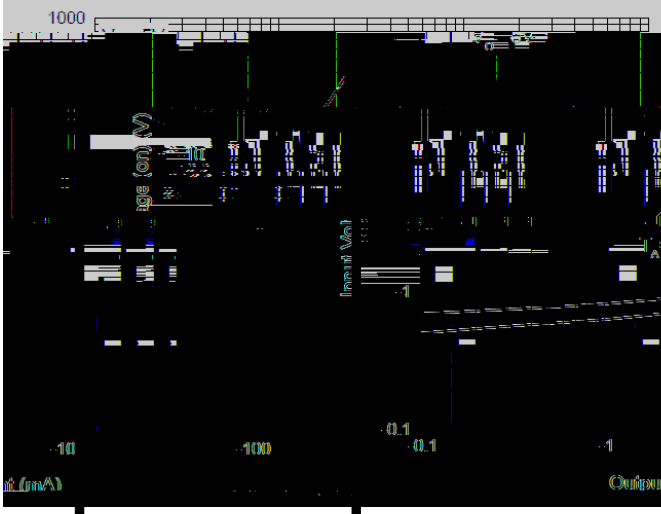


Fig. 2 - Input Voltage (on) Characteristics

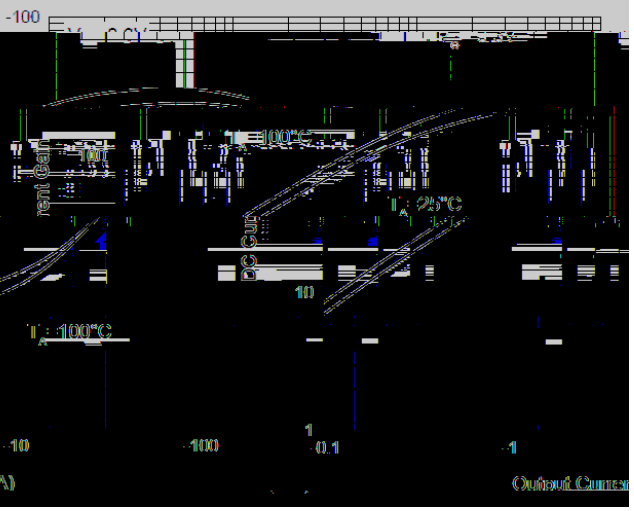


Fig. 3 - Input Voltage (off) Characteristics

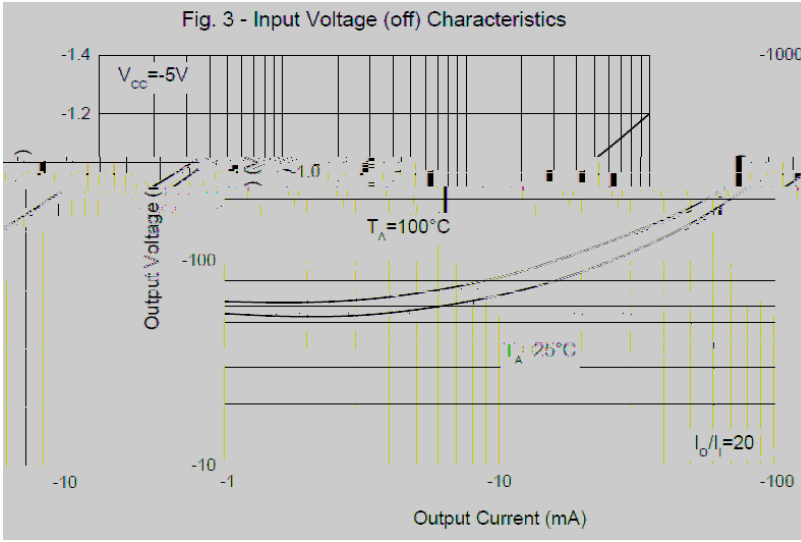
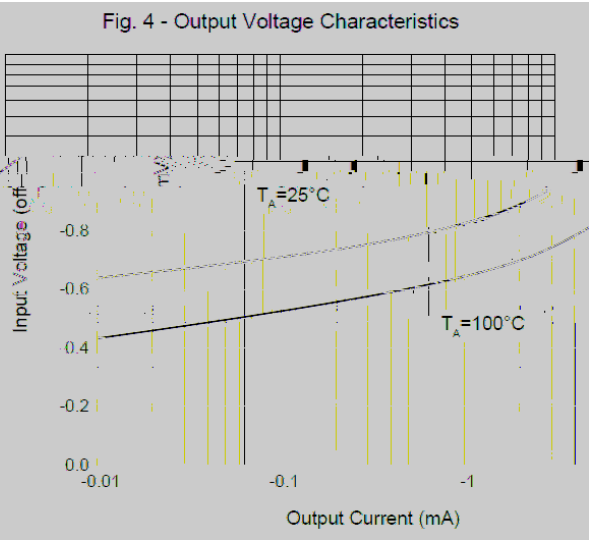
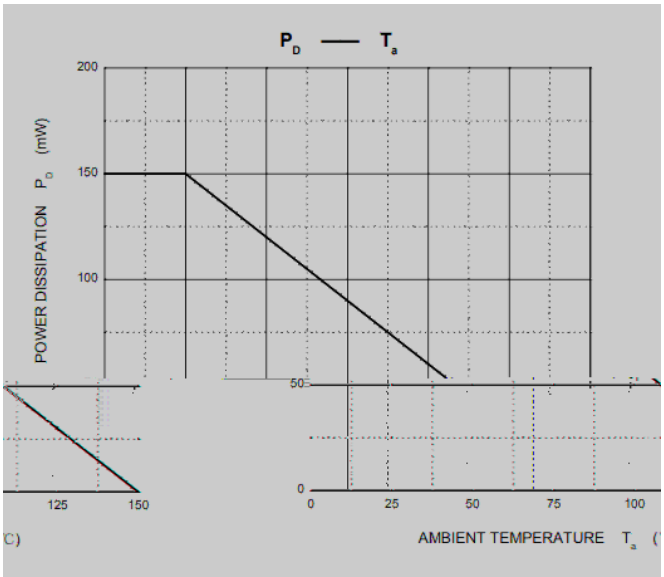
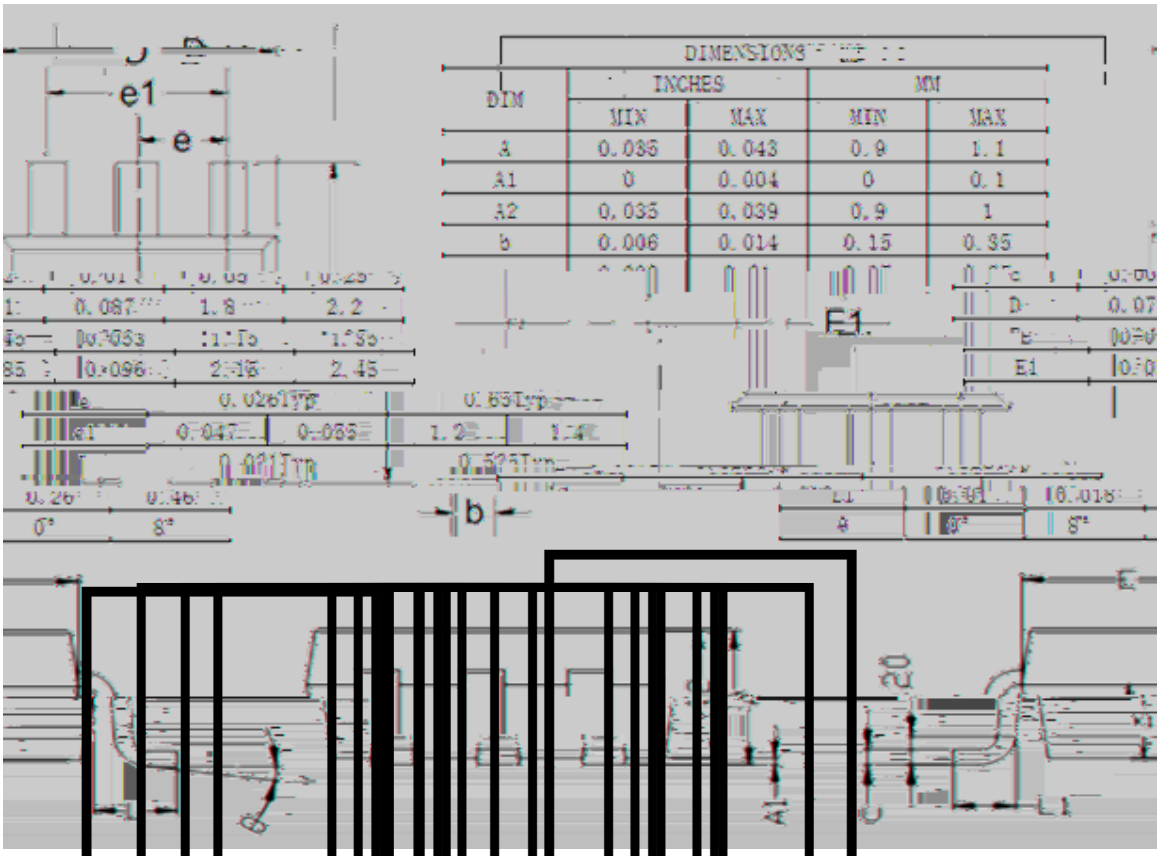


Fig. 4 - Output Voltage Characteristics

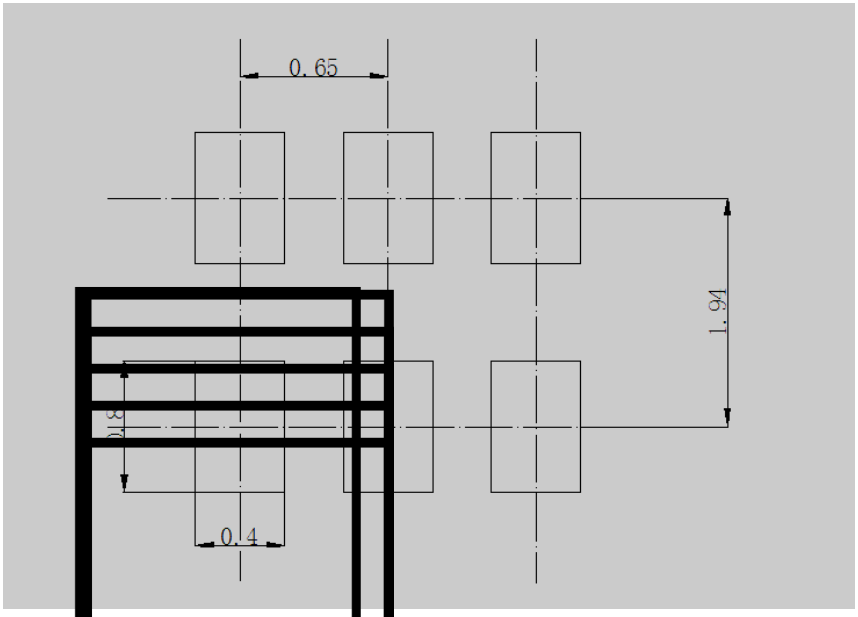


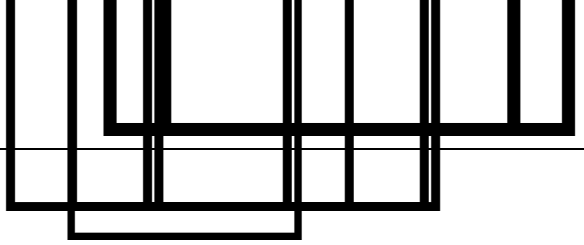


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e1
e
DIMENSIONS
DIM
INCHES
MM
MIN
MAX
MIN
MAX
A
0.035
0.043
0.9
1.1
A1
0
0.004
0
0.1
A2
0.035
0.039
0.9
1
b
0.006
0.014
0.15
0.35
E1
b
A1
L1

■


0.65
1.94
0.4



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