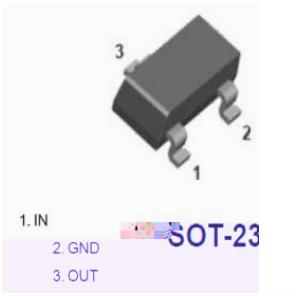
DTA123YCA

RoHS
COMPLIANT

PNP Digital Transistors (Built-in Resistors)



[redacted]

Features

-
-
-

Application

-
-

Mechanical data

- **Package**
- **Terminals**

Maximum Ratings

Item	Symbol	Unit	Conditions	Value

Electrical Characteristics

[redacted]	Symbol	Unit	Conditions	Min	Typ	Max

Characteristics

Fig 1 Input Voltage (On) Characteristics

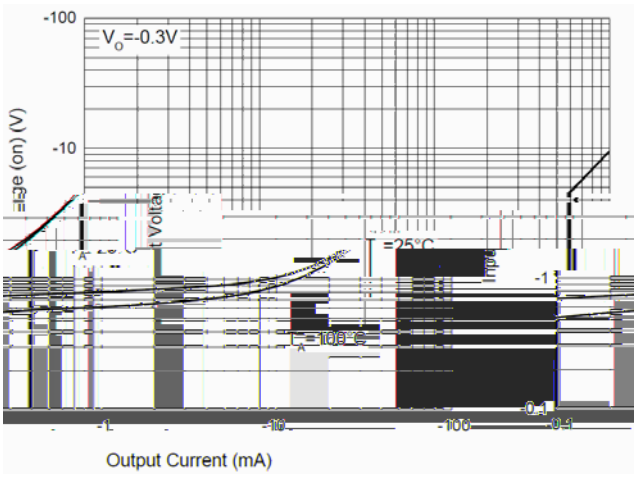


Fig 2 Input Voltage (Off) Characteristic

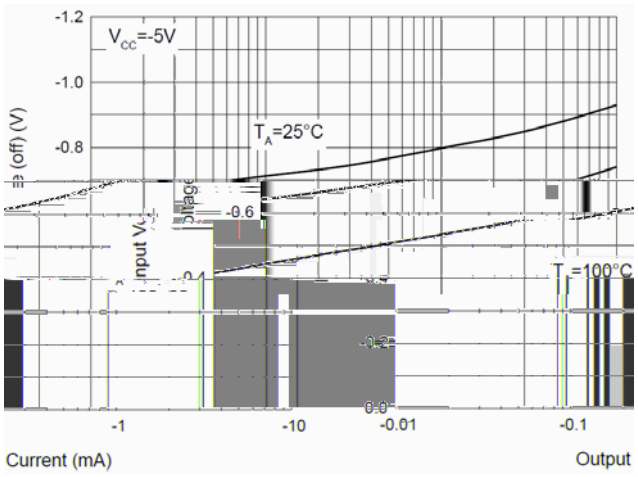


Fig 3 DC Current Gain Characteristics

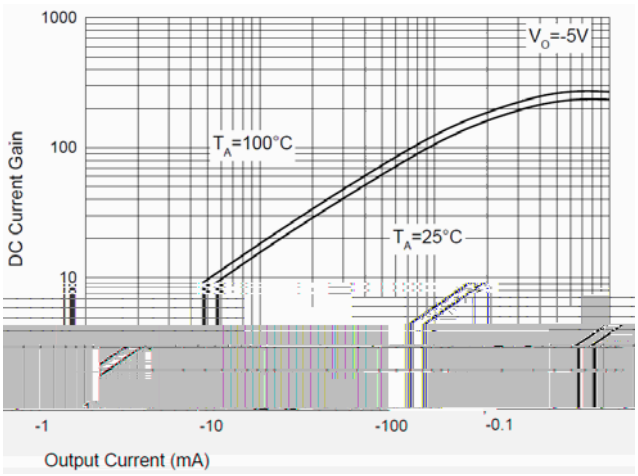


Fig 4 Output Voltage Characteristics

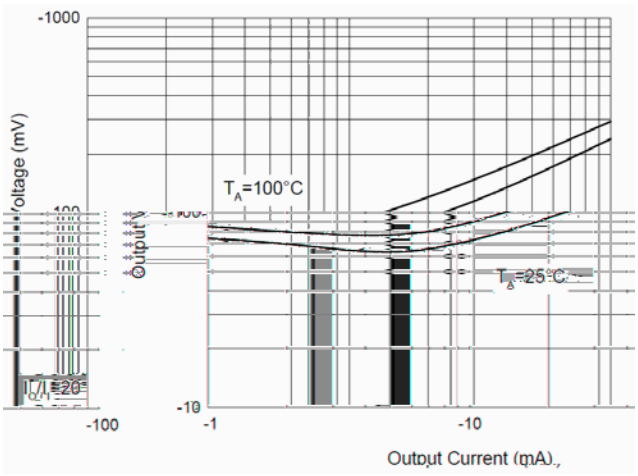
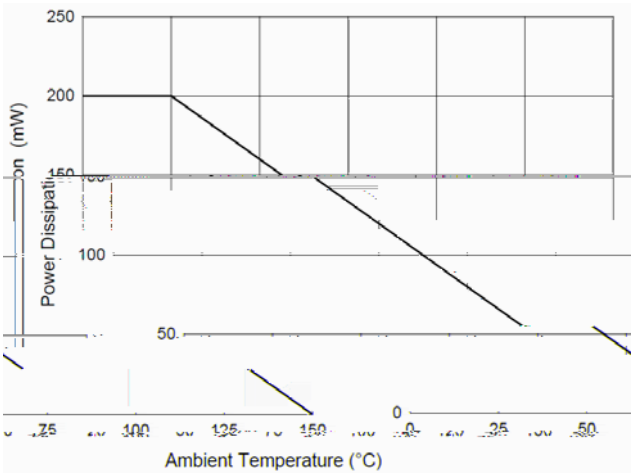
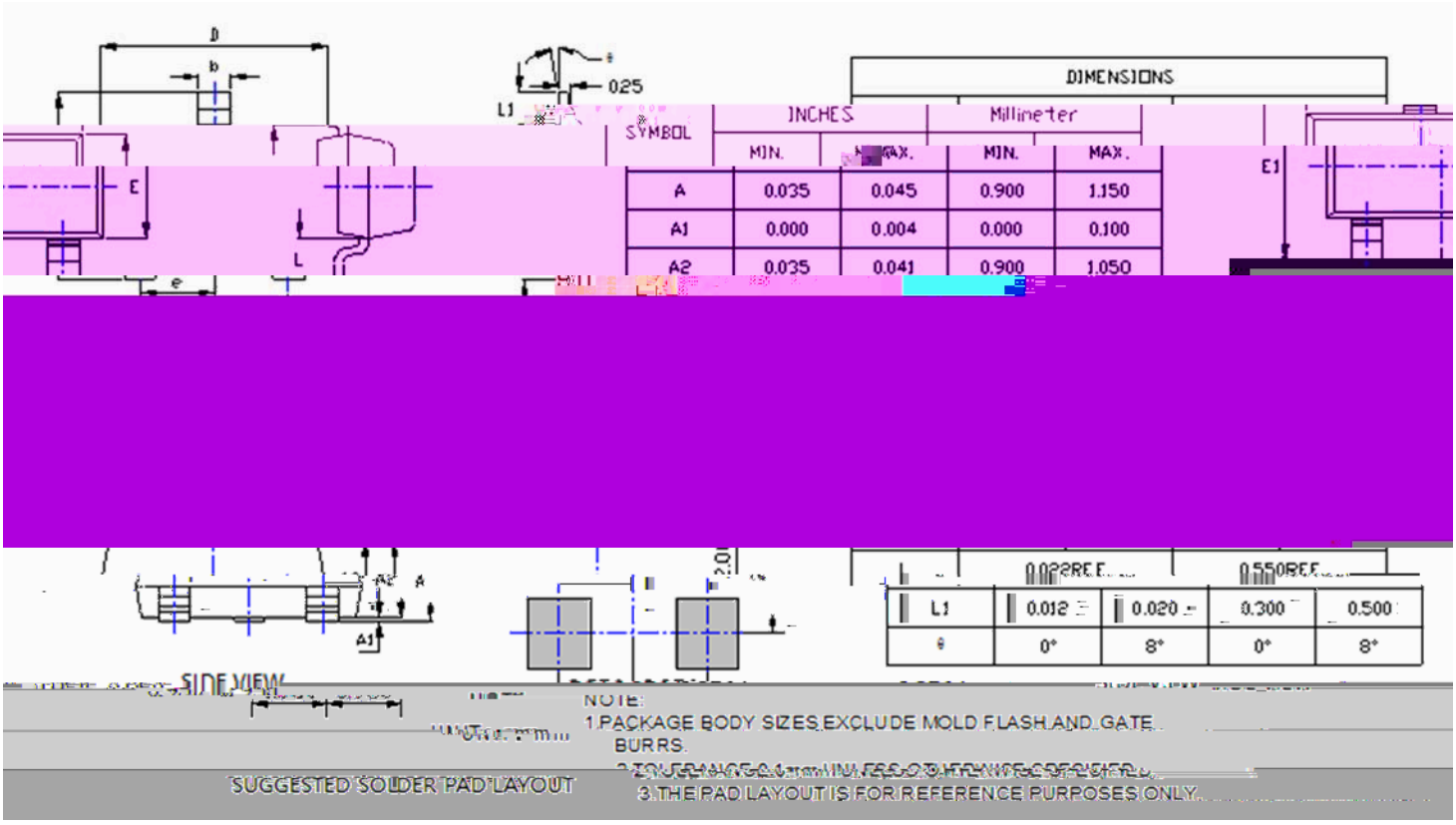


Fig 5 P_D-Ta Curve



Outline Dimensions



Note:

1. All dimensions are in millimeters (mm) unless otherwise specified.
[所有尺寸均以毫米为单位，除非另有说明]
2. General tolerances: ±0.10mm unless otherwise specified.
[通用公差为±0.10mm，除非另有说明]
3. Dimensions and tolerances per ASME Y14.5M–2018.
[尺寸和公差遵循ASME Y14.5M–2018标准]
4. All dimensions shown are exclusive amounts of gate residues, burrs or gate vestiges shall not exceed 0.15 mm in maximum.
[所有尺寸均不包括毛刺和浇口残留。毛刺与浇口残留的尺寸最大不得超过 0.15mm]
5. Dimension b does not include dambar protrusion of max 0.100 mm per side.
[尺寸b不包括单边最大0.100 MM的中筋凸出部分]
6. Dimensions D and E1 do not include mold protrusion. Allowable protrusion is 0.25 mm per side. These dimensions exclude mold flash, lead flash, protrusions and burrs but include the maximum allowable mold mismatch.
[尺寸D和E1不包括模具突出部分。允许的突出部分为每侧0.25 mm。这些尺寸不包括模具飞边、引线飞边、突出部分和毛刺，但包含了允许的最大模具错位]
7. Formed leads shall be planar with respect to one another within 0.076 mm relative to the seating plane.
[成型的管脚应为同一平面共面性最大为0.1mm]
8. ★It is the key size.
[★ 标记为关键尺寸]

Disclaimer