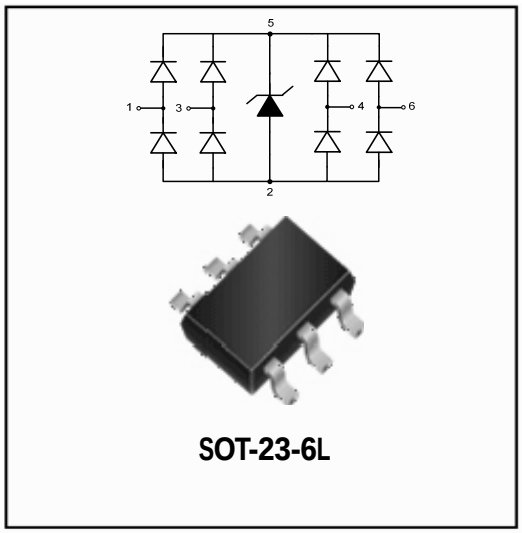
4-Line, Uni-directional, Ultra-low Capacitance, Transient Voltage Suppressor

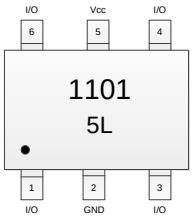


Features

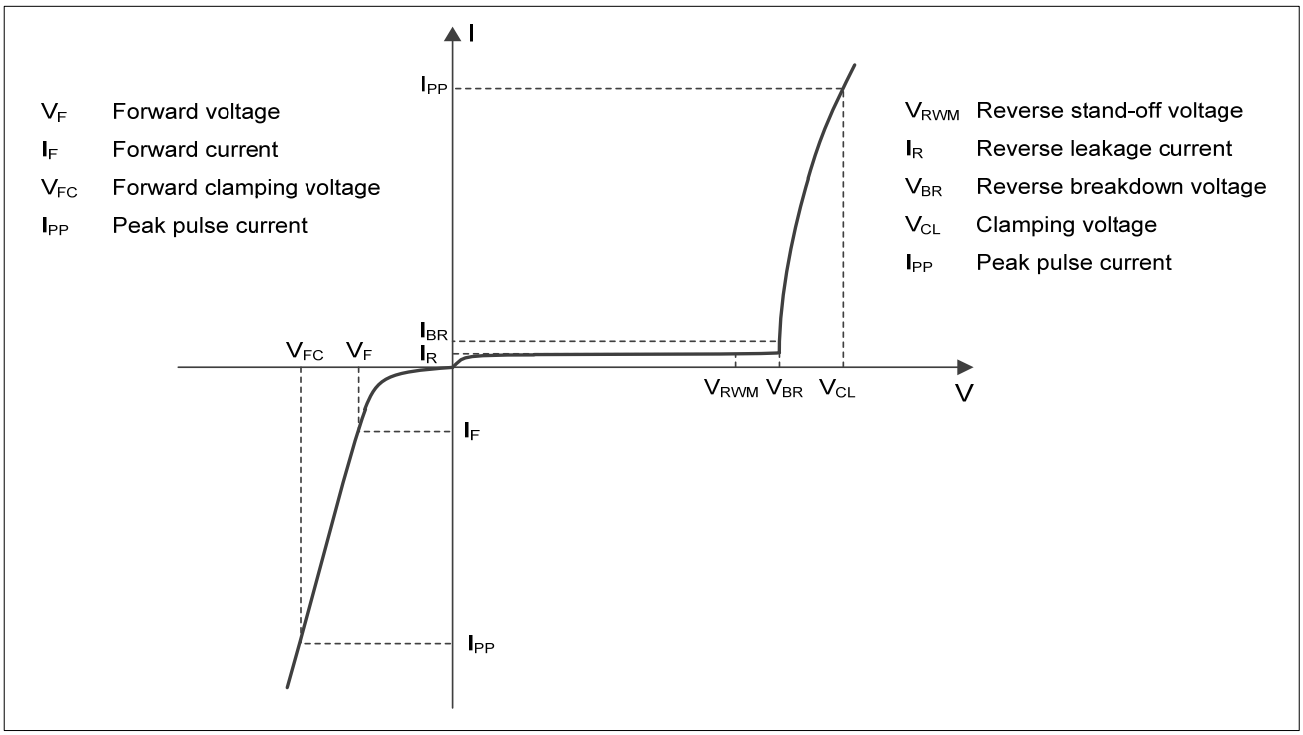
- Stand-off voltage: 5V Max
- Transient protection for each line according to IEC61000-4-2(ESD): ±20kV (contact) IEC61000-4-5(surge): 3.5A (8/20μs)
- Ultra-low capacitance: C_J = 0.4pF typ.
- Low leakage current: I_R <1nA typ.
- Low clamping voltage: V_{CL} = 14.0V typ. @ I_{PP} = 16A (TLP)
- Solid-state silicon technology

Mechanical Data

- **Package**: SOT-23-6L
- **Terminals**: Tin plated leads, solderabl per J-STD-002 and JESD22-B102
- **Polarity:** Cathode line denotes the cathode end
- **Marking:**



■Definitions of electrical characteristics



■Maximum Ratings

PARAMETER	SYMBOL	LIMITS	UNIT
Peak pulse power (t_p = 8/20μs)	P_{pk}	42	W
Peak pulse current (t_p = 8/20μs)	I_{PP}	3.5	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	±20	KV
ESD according to IEC61000-4-2 contact discharge		±20	
Junction temperature	T_J	125	°C
Operating temperature	T_{OP}	-40~85	°C
Storage temperature	T_{STG}	-55~150	°C

■Electrical Characteristics T_a=25 Unless otherwise specified

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				5
Reverse leakage current	I_R	nA	V_{RWM} = 5V		<1	100
Reverse breakdown voltage	$V_{(BR)}$	V	I_T = 1mA	7	8	9
Forward voltage	V_F	V	I_T = 10mA	0.6	0.9	1.2
Clamping voltage 1)	V_{CL}	V	I_{PP} = 16A, t_p = 100ns		14	
Dynamic resistance 1)	R_{DYN}	Ω			0.33	
Clamping voltage 2)	V_{CL}	V	V_{ESD} = 8kV		14	
Clamping voltage 3)	V_{CL}	V	I_{PP} = 1A, t_p = 8/20μs			10
		V	I_{PP} = 3.5A, t_p = 8/20μs			12
Junction capacitance	CJ	pF	V_R = 0V, f = 1MHz Any I/O pin to GND		0.4	0.65
			V_R = 0V, f = 1MHz Between any I/O pin		0.25	0.4

Notes:

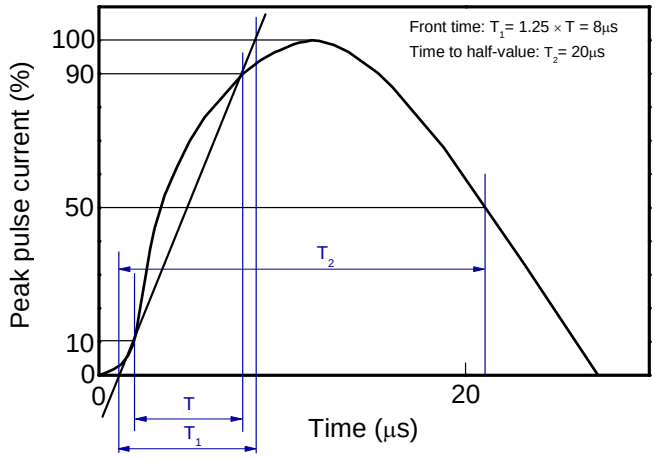
1) TLP parameter: Z_0 = 50Ω, t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
2) Contact discharge mode, according to IEC61000-4-2.
3) Non-repetitive current pulse, according to IEC61000-4-5.

■Ordering Information (Example)

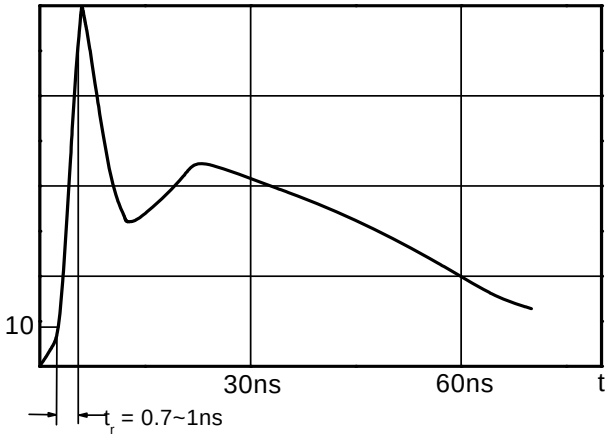
PREFERED P/N	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESDSLC5V0E	3000	30000	120000	7 reel

■ Characteristics (Typical)

8/20 s waveform per IEC61000-4-5



Contact discharge current waveform per IEC61000-4-2



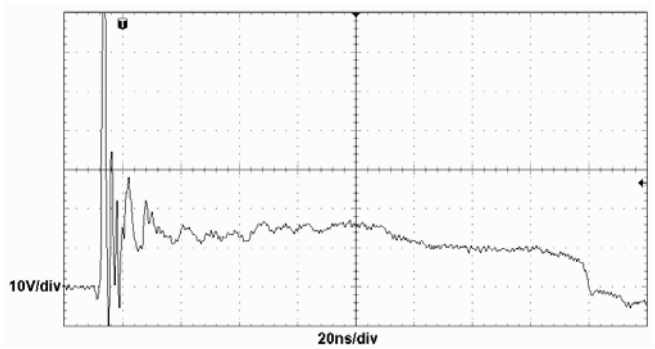
Clamping voltage vs. Peak pulse current

Capacitance vs. Reverse voltage

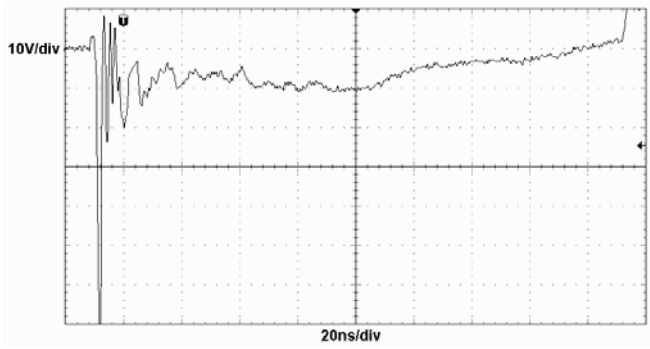
Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

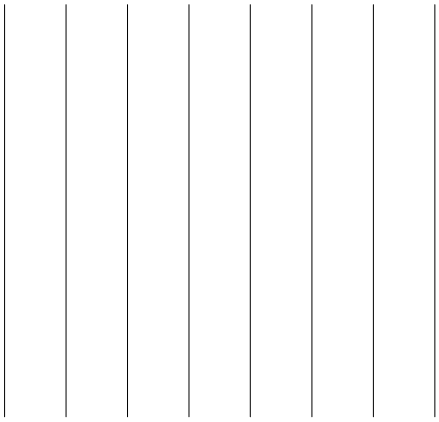
ESD clamping
(+8kV contact discharge per IEC61000-4-2)



ESD clamping
(-8kV contact discharge per IEC61000-4-2)



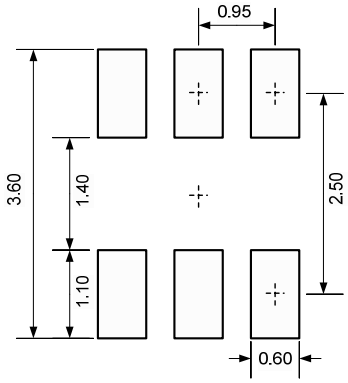
TLP Measurement



■ Outline Dimensions

Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	1.05	-	1.45
A1	0	-	0.15
A2	0.90	1.10	1.30
b	0.30	0.40	0.50
c	0.10	-	0.21
D	2.72	2.92	3.12
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.85	0.95	1.05
e1	1.80	1.90	2.00
L	0.30	-	0.60
L1	0.59 BSC		
L2	0.25 BSC		
θ	0	-	8

■ Soldering Footprint



Notes:
This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

Disclaimer

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