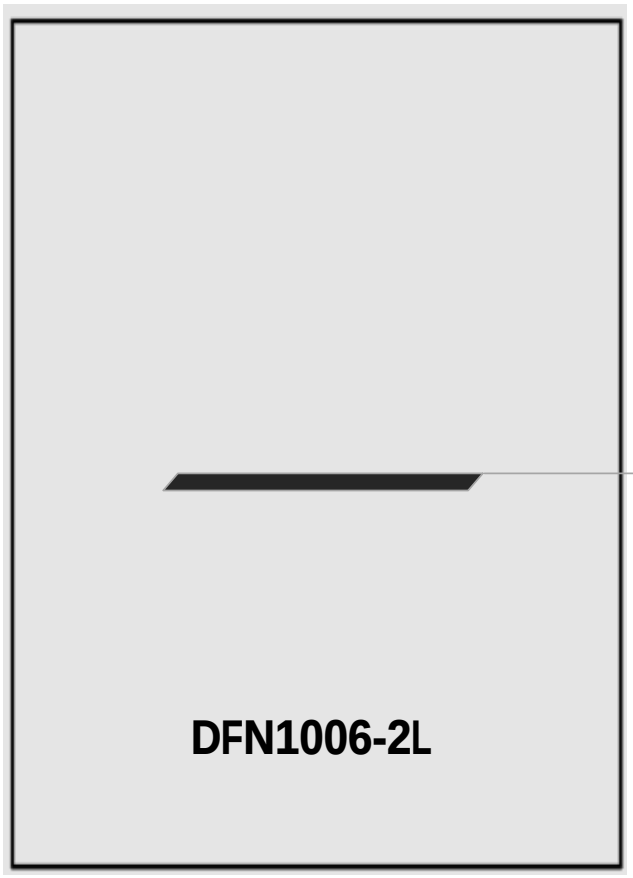


- Stand-off voltage: 3.3V Max
- Transient protection for each line according to
  IEC61000-4-2(ESD): ±30kV (contact)
  IEC61000-4-5(surge): 18A (8/20μs)
- Low capacitance: CJ = 2.5pF typ
- Ultra-low leakage current
- Low clamping voltage:
V_{CL} = 3.1V typ. @ IPP = 16A (TLP)
- RoHS Compliant

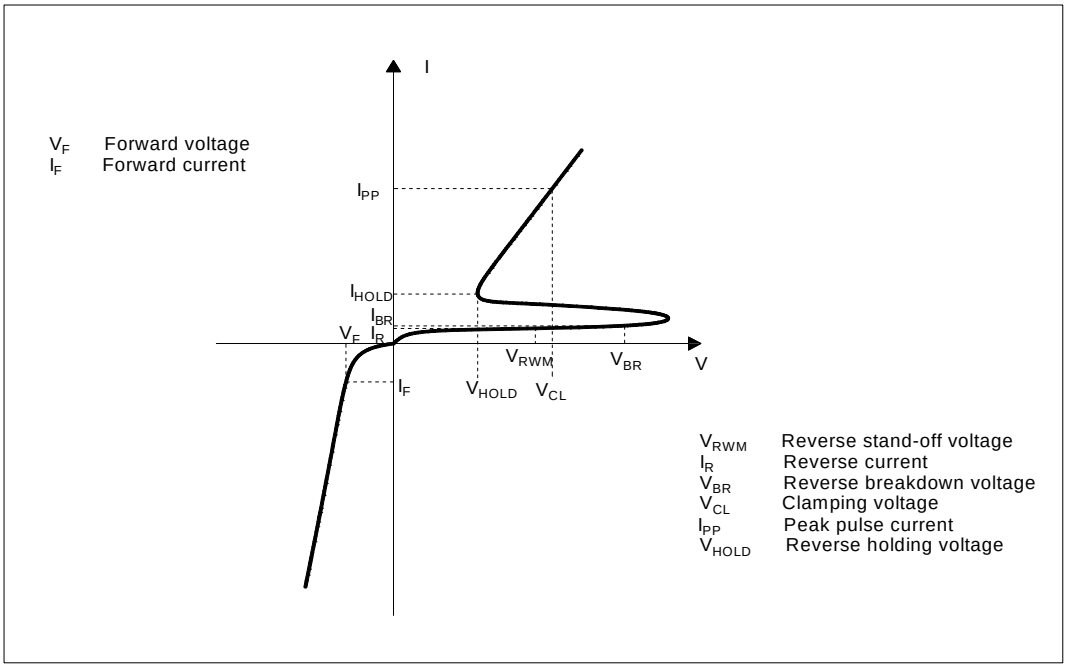
- USB 2.0
- HDMI 1.3, HDMI 1.4
- SATA and eSATA interface
- DVI
- IEEE 1394
- Portable Electronics and Notebooks

Caution:
This Device is designed for signal line protection only. Not intended to be used under bias, not for application with a power line.

- Package: DFN1006-2L
- Case Material: “Green” Molding Compound
- Moisture Sensitivity: Level 3 per J-STD-020
- Marking Information: See Below

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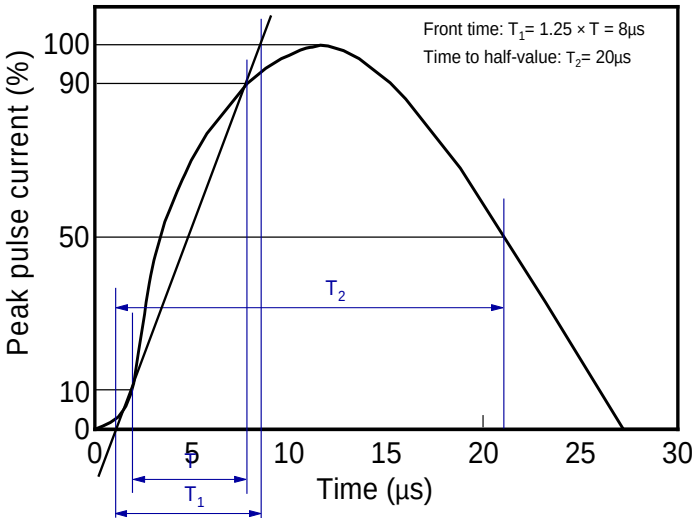
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Peak pulse power (t_p = 8/20μs)	P_{pk}	90	W
Peak pulse current (t_p = 8/20μs)	I_{PP}	18	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	±30	kV
ESD according to IEC61000-4-2 contact discharge		±30	
Junction temperature	T_J	-55~125	°C
Storage temperature	T_{STG}	-55~150	°C

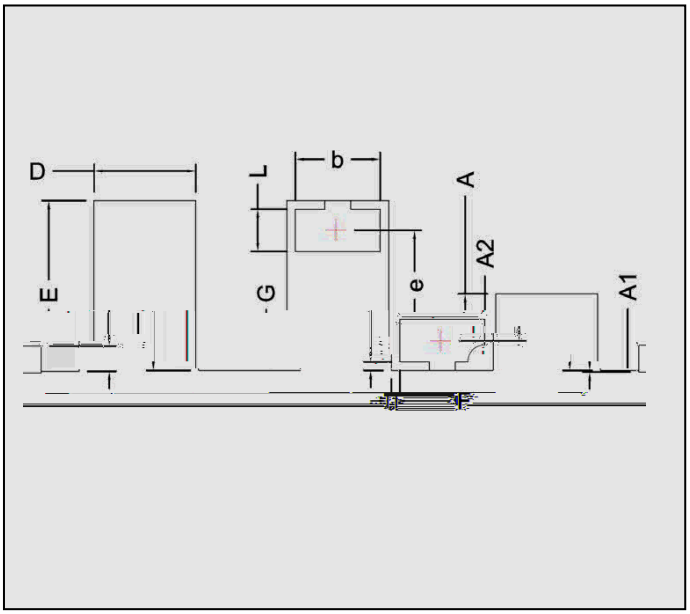
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Reverse maximum working voltage	V_{RWM}	V				3.3
Reverse leakage current	I_R	nA	V_{RWM} = 3.3V			50
Reverse breakdown voltage	V_{BR}	V	I_T =1mA	8.5		
Reverse holding voltage	V_{HOLD}	V	I_{HOLD} = 50mA,	1.8		
Clamping voltage *1)*	V_{CL}	V	I_{PP} = 16A, t_p = 100ns		4.2	
Dynamic resistance *1)*	R_{DYN} `ƒc 3r0 •	Ω			0.06	
Clamping voltage *2)*	V_{CL}	V	V_{ESD} = 8kV		5	
Clamping voltage *3)*	V_{CL}	V V	I_{PP} = 1A, t_p = 8/20μs I_{PP} = 18A, t_p		2.3	3.0

■


Front time: T_1= 1.25 × T = 8μs
Time to half-value: T_2= 20μs
Peak pulse current (%)
100
90
50
10
0
0
5
10
15
20
25
30
Time (μs)
T_2
T
T_1

■



	0.50	0.60	0.70
	0.90	1.00	1.10
	0.35	0.45	0.55
	0.15 BSC		
			0.10
	0.005		
	0.005		
	0.15	0.25	0.35
	0.41	0.50	0.59
	0.65 BSC		

Notes:
This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

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